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 INFORMATION DISCLOSURE STATEMENT <small>BEST</small> <small>JAN 08 2002</small> <small>PATENT & TRADEMARK OFFICE</small>			Docket: 1011-58140	App: 09/872,212
			Applicant: Kebichi et al.	
			Filed: May 31, 2001	Art Unit: <u>2818</u> <u>2133</u>
OTHER DOCUMENTS				
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	MBISTArchitect Reference Manual, V8.8_1.100, September 2000, Chapter 1 - Introduction	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	MBIST Architect – Anatomy of the BIST Controller, published by Mentor Graphics Corporation, April 1999	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	MBISTArchitect Reference Manual, V8.9_1.10, Defining the Model – DFT Library Modeling for Memories, December 2000	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	MBIST Architect – UDA Kernel Design, published by Mentor Graphics, April 1999	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Burgess, "Test and Diagnosis of Embedded Memory Using BIST," <i>EE-Evaluation Engineering</i> , March 2000.	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Mentor Graphics, "Built-In Self-Test Process Guide, Software Version 8.9_1.10," December 2000.	
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			FEB 07 2002	
			Technology Center 2100	
EXAMINER:			DATE <u>2/28/04</u>	
<p>*Examiner: Initial if considered, whether or not in conformance with MPEP 609; draw line through cite if not in conformance and not considered. Send copy.</p>				